ABSTRACT OF THE DISCLOSURE

To an organic EL element 21-of a pixel circuit 20-provided in association with an intersection of a scanning line Yn-and a data line Xm, line, a driving current in accordance with digital data VDm or an analog data current IAm is supplied via the data line Xm. line. When halftones are controlled by digital gray scale in order to reduce power consumption, digital data VDm having a value corresponding to either H level a first level or L level a second level is supplied to the pixel eireuit 20. circuit. When halftones are controlled by analog gray scale in order to improve display quality, an analog data current IAm is supplied to the pixel eireuit 20.circuit.